2.1 ± 0.1

1.25 ± 0.1

0~0.1

5.Gate 2

2-2J1C

1.Source 1 4.Source 2

3.Drain 2 6.Drain 1

2.Gate 1

US6

JEDEC JEITA TOSHIBA Unit: mm

TOSHIBA Field-Effect Transistor Silicon N-Channel MOS Type

SSM6N35FU

- High-Speed Switching Applications
- Analog Switch Applications
- 1.2-V drive
- N-ch 2-in-1

• Low ON-resistance: $R_{DS(ON)} = 20 \Omega \text{ (max) } (@V_{GS} = 1.2 \text{ V})$

 $R_{DS(ON)} = 8 \Omega \text{ (max) } (@V_{GS} = 1.5 \text{ V})$ $R_{DS(ON)} = 4 \Omega \text{ (max) } (@V_{GS} = 2.5 \text{ V})$

 $R_{DS(ON)} = 3 \Omega \text{ (max) } (@V_{GS} = 4.0 \text{ V})$

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

| Characteristics | | Symbol | Rating | Unit | |
|----------------------|-------|-------------------------|------------|------|--|
| Drain-source voltage | | V_{DS} | 20 | V | |
| Gate-source voltage | | V _{GSS} | ± 10 | V | |
| Drain current | DC | ID | 180 | mA | |
| | Pulse | I _{DP} | 360 | | |
| Power dissipation | | P _D (Note 1) | 200 | mW | |
| Channel temperature | | T _{ch} | 150 | °C | |
| Storage temperature | | T _{stg} | -55 to 150 | °C | |

Weight: 6.8 mg (typ.)

1.3±0.1

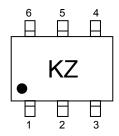
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly

even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

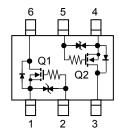
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Total rating

Marking



Equivalent Circuit (top view)

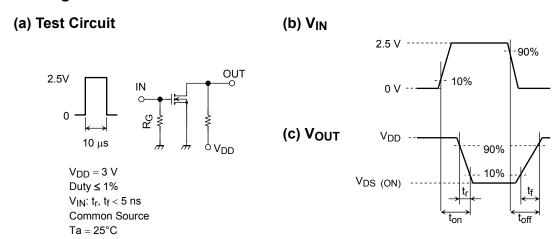


Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

| Chara | acteristics | Symbol | Test Condition | | Min | Тур. | Max | Unit |
|------------------------------|---------------|----------------------|---|----------|-----|------|------|------|
| Gate leakage curre | ent | I _{GSS} | $V_{GS} = \pm 10 \text{ V}, V_{DS} = 0 \text{ V}$ | | _ | _ | ±10 | μА |
| Drain-source brea | kdown voltage | V (BR) DSS | $I_D = 0.1 \text{ mA}, V_{GS} = 0 \text{ V}$ | | 20 | _ | _ | V |
| Drain cutoff curren | t | I _{DSS} | V _{DS} = 20 V, V _{GS} = 0 V | | _ | _ | 1 | μА |
| Gate threshold vol | tage | V _{th} | $V_{DS} = 3 \text{ V}, I_D = 1 \text{ mA}$ | | 0.4 | _ | 1.0 | V |
| Forward transfer a | dmittance | Y _{fs} | $V_{DS} = 3 \text{ V}, I_{D} = 50 \text{ mA}$ | (Note 2) | 115 | _ | _ | mS |
| Drain–source ON-resistance | | R _{DS} (ON) | $I_D = 50 \text{ mA}, V_{GS} = 4 \text{ V}$ | (Note 2) | _ | 1.5 | 3 | Ω |
| | | | $I_D = 50 \text{ mA}, V_{GS} = 2.5 \text{ V}$ | (Note 2) | _ | 2 | 4 | |
| | | | I _D = 5 mA, V _{GS} = 1.5 V | (Note 2) | _ | 3 | 8 | |
| | | | I _D = 5 mA, V _{GS} = 1.2 V | (Note 2) | _ | 5 | 20 | |
| Input capacitance | | C _{iss} | $V_{DS} = 3 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | | | 9.5 | _ | pF |
| Reverse transfer capacitance | | C _{rss} | | | _ | 4.1 | _ | |
| Output capacitance | | Coss | | | _ | 9.5 | _ | |
| Switching time | Turn-on time | t _{on} | V _{DD} = 3 V, I _D = 50 mA | _ | 115 | _ | - ns | |
| | Turn-off time | t _{off} | V _{GS} = 0 to 2.5 V | | _ | 300 | | _ |
| Drain-source forward voltage | | V _{DSF} | $I_D = -180 \text{ mA}, V_{GS} = 0 \text{ V}$ | (Note 2) | | -0.9 | -1.2 | V |

Note 2: Pulse test

Switching Time Test Circuit



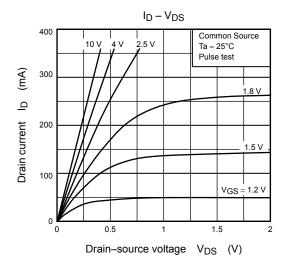
Handling Precaution

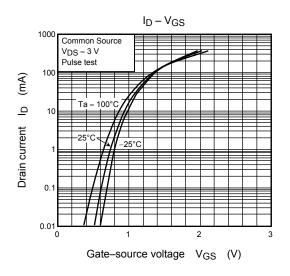
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

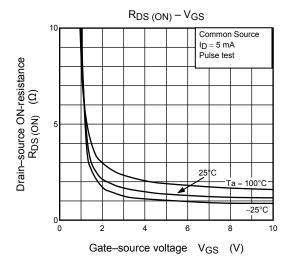
Usage Considerations

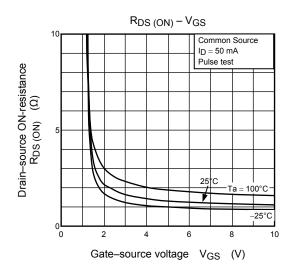
Let V_{th} be the voltage applied between gate and source that causes the drain current (I_D) to below (1 mA for the SSM6N35FU). Then, for normal switching operation, $V_{GS(on)}$ must be higher than V_{th} , and $V_{GS(off)}$ must be lower than V_{th} . This relationship can be expressed as: $V_{GS(off)} < V_{th} < V_{GS(on)}$.

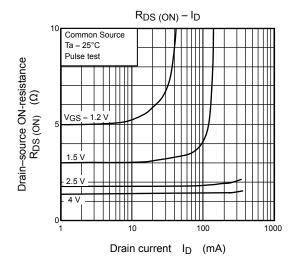
Take this into consideration when using the device.

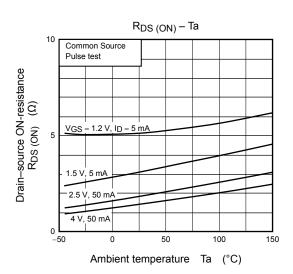


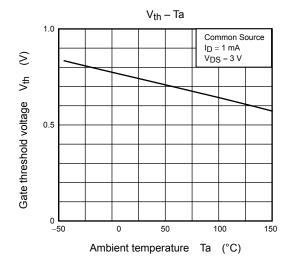


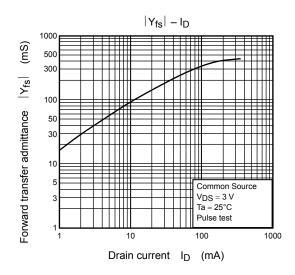


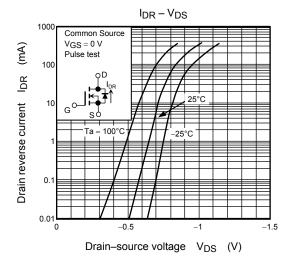


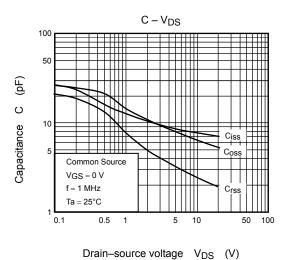


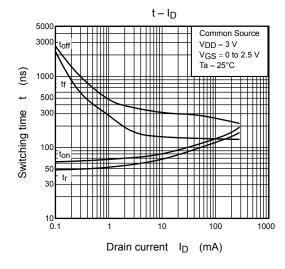


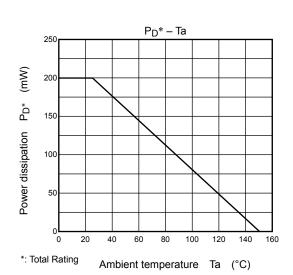












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